

AMENDMENTS TO THE SPECIFICATION

On page 1, please delete lines 4 and 5 and substitute therefor the following:

~~This application is a continuation-in-part of pending U.S. application Serial No. 08/087,932, filed July 6, 1993.~~ This application is a continuation of application Serial No. 10/166,805, filed 06/11/2002, which is a continuation of application Serial No. 08/834,774, filed 04/03/1997, now Patent No. 6,424,799, which is a continuation of serial No. 08/369,497, filed 01/06/1995, now abandoned, which is a continuation-in-part of serial No. 08/087,932, filed 07/06/1993, now Patent No. 5,440,215.

Please replace the following paragraphs of the specification. Please delete the first full paragraph on page 33 of the specification and replace it with the following:

Thus, it will be appreciated that the control circuit produces a substantially square wave output signal at OUTPUT pin 3, the duty cycle of which is controlled by the setting of trigger resistor R1. Note, however, that the frequency of the duty cycle signal remains constant as a ~~change~~ change in the position of trigger resistor R1 increasing the ~~change~~ charge time of the capacitor C1 causes a corresponding decrease in the discharge time of the capacitor C1, and vice versa. In other words, if the duration of the HI signal at pin 3 is increased by a given amount, the duration of the corresponding LO signal is decreased by the same amount. Consequently, the frequency of the output signal, which is determined by the combined period of the HI and LO signals, remains the same.